



09/747,848
Response to Office Action mailed January 16, 2002

PATENT

#8/a
4-25-2
Euler

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Vladislav Vashchenko et al.

Appln. No.: 09/747,848

Filed: December 21, 2000

For: DIODE JUNCTION BASED
ELECTROSTATIC DISCHARGE (ESD) PROTECTION STRUCTURE

Group Art Unit: 2811

Examiner: O. Nadav

RESPONSE TO OFFICE ACTION MAILED
JANUARY 16, 2002

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service, postage prepaid, in an envelope, addressed to Box Non-Fee Amd. Commissioner for Patents, Washington D.C. 20231-9999 on 04-01-02.

Commissioner for Patents
Washington, D.C. 20231

Dated: 04-01-02

By: Debra R. King

Dear Sir:

In response to the Official Action mailed January 16, 2002, please amend the above-identified application as follows:

In the Claims

The claims have been amended to read as follows:

- sub B.1
Q1
1. (Amended) An ESD protection structure for an integrated circuit comprising:
 - a semiconductor substrate of a first conductivity type;
 - a well region of a second conductivity type disposed in the semiconductor substrate;
 - a first region of the first conductivity type disposed in the well region on the semiconductor substrate;
 - a second region of the second conductivity type disposed in and on the semiconductor substrate and spaced apart from the first region and the well; and
 - an electrical isolation region disposed in the semiconductor substrate between the first region and the second region.

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